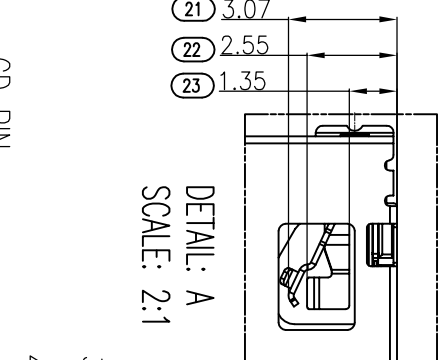
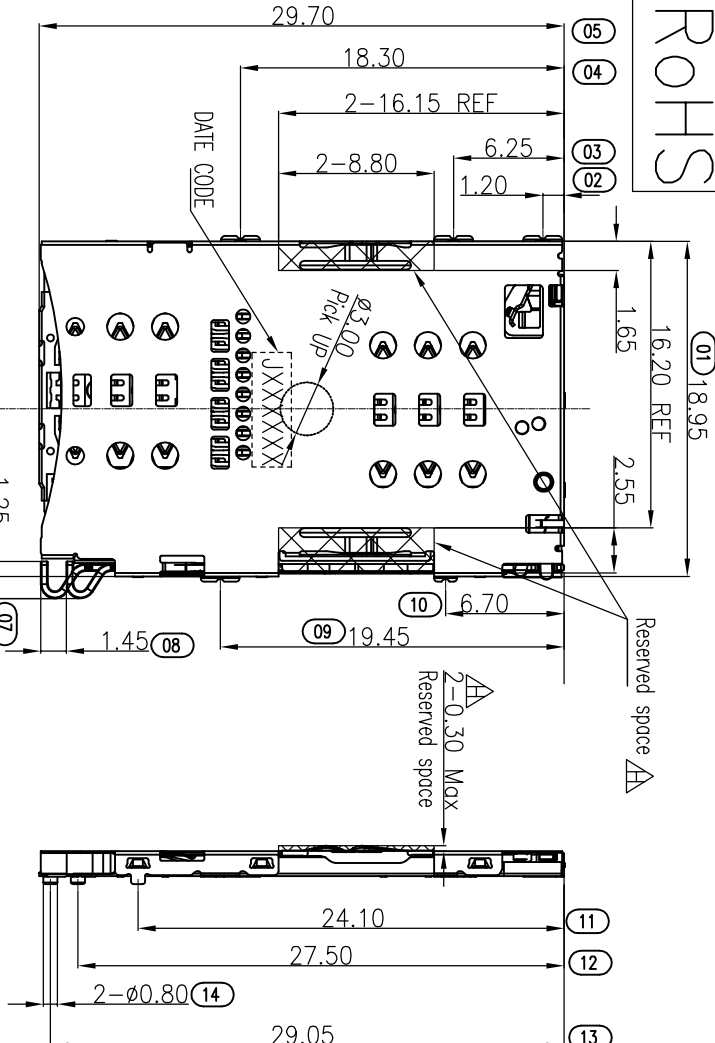


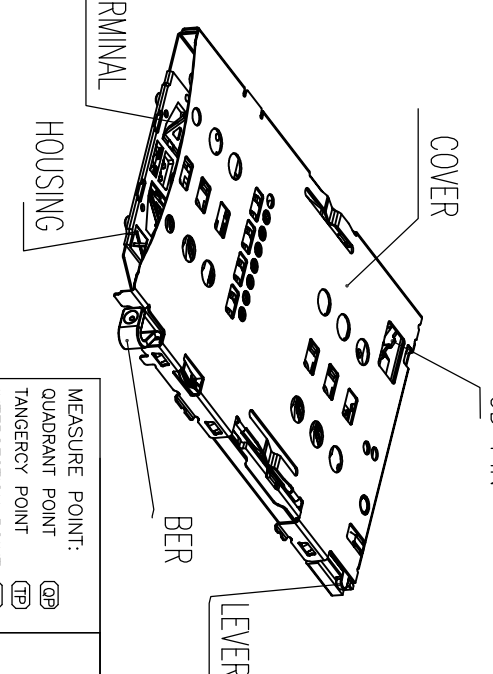
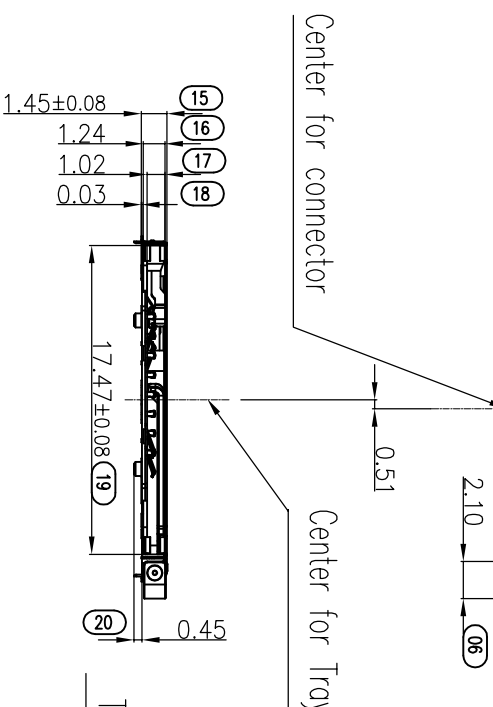
ROHS



DETAIL: A
SCALE: 2:1

REV.	LOCAS.	DESCRIPTION	DRAW.	DATE.
H	MODIFY	ECR-1603017	Sam	03/20'16
I	MODIFY	ECR-1605012	Sam	04/18'16
J	MODIFY	ECR-1605020	Sam	05/25'16

- NOTES:
1. MATERIAL: SEE TABLE(Housing can't add any recycled material)
 2. SPECIALITY:
 - 2.1 Rated current:1.0A
 - 2.2 Rated voltage:30V
 - 2.3 Contact Resistance:50mohm MAX
 - 2.4 Insulation Resistance:1000Mohm MIN 500V DC
 - 2.5 Dielectric withstanding voltage: 500V DC.
 - 2.6 Solder ability:250±5?? 10±0.5s.
 - 2.7 Durability:3000 Cycles
 - 2.8 Mating and Unmating force Insertion Force 10N Max; Withdrawal Fore 2N Min
 - 2.9 Operating condition: Temperature-40?? +85?? Humidity 80% R.H MAX
 3. COPPLANARITY: BEFORE IR 0.08mm Max AFTER IR 0.08 mm Max
 4. PLEASE CONTACT JITS SALES REPRESENTATIVE TO VERIFY PRODUCT DETAILS & AVAILABILITY



NO.	PART NAME	PART NO.	QUANTITY	COLOR/MATERIAL
6	CD PIN		1PCS	C5240
5	BAR		1PCS	SUS304-3/4H
4	LEVER		1PCS	SUS301-1/2H
3	SHELL		1PCS	SUS304-3/4H
2	TERMINAL		1SET	C5240
1	HOUSING		1PCS	LCP BLACK

TABLE

MEASURE POINT:
 QUADRANT POINT (QP)
 TANGENCY POINT (TP)
 INTERSECTION POINT (IP)
 UNLESS OTHERWISE SPECIFIED, TOLERANCE:
 X ±0.25
 X.X ±0.20
 X.XX ±0.15
 ANGULAR ±3°

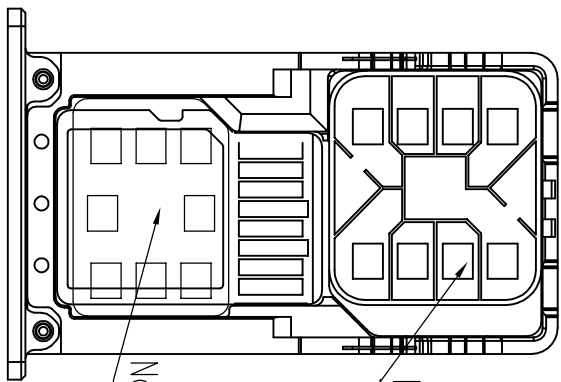
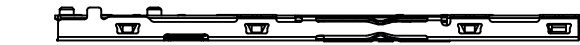
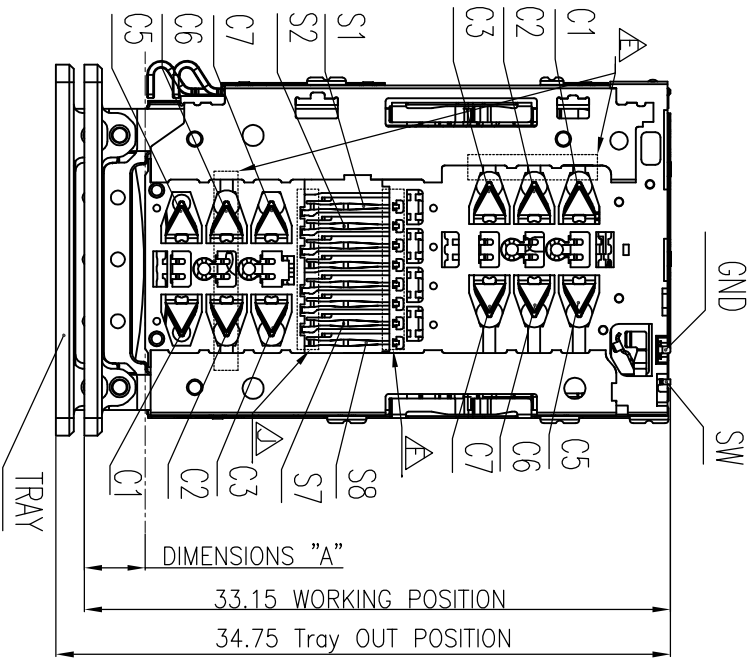
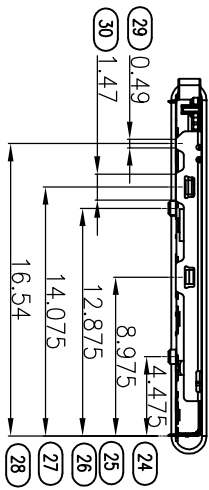
JYS 杰宇森电子有限公司
JIE YU SEN ELECTRONIC CO., LTD.

DESIGN	DATE	TITLE:
CHECKED	DATE	MICRO SIM+MICRO SD/Nano SIM CONNECTOR(20pin) 145mm HEIGHT
APPROVED	DATE	

PART NO.:	CONNECTOR	PART NO.:	PLATING
JYS-SIM145-128	MICRO SIM+MICRO SD/Nano SIM CONNECTOR(20pin) 145mm HEIGHT	JYS-SIM145-128	Shell Solder: 6/f: 30u"Ni PLATING CONTACT: 50%Ni, solder: 1u"Ni, 80u"Ni PLATING HOUSING(No add any recycling material)

DRAWING NO.:	REV.:	SIZE:	SHEET:	SCALE:	UNIT:
JYS-SIM145-128	J	A4	1/4	1:1	mm

ROHS



Micro SIM Circuit Description:

PIN No.	FUNCTION
C1	VCC
C2	RST
C3	CLK
C4	RESERVED
C5	GND
C6	VPP
C7	I/O
C8	RESERVED

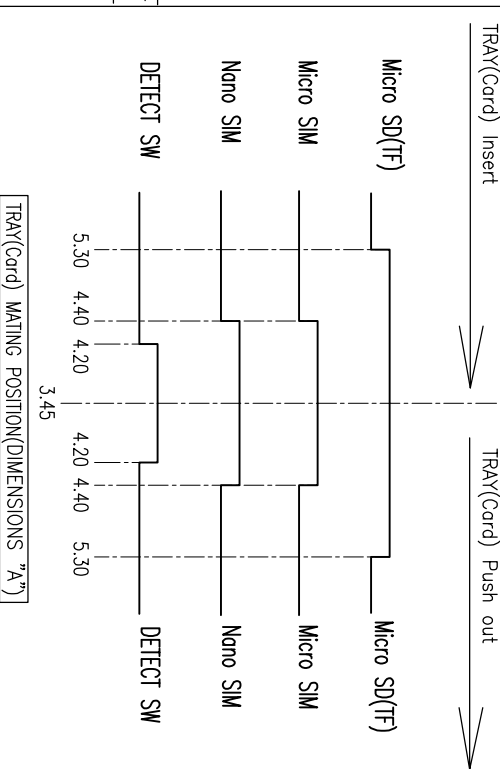
Nano SIM Circuit Description:

PIN No.	FUNCTION
C1	VCC
C2	RST
C3	CLK
C4	RESERVED
C5	GND
C6	VPP
C7	I/O
C8	RESERVED

Micro SD Circuit Description:

PIN No.	FUNCTION
S1	DAT2
S2	CT/DAT3
S3	CMD
S4	VDD
S5	CLK
S6	VSS
S7	DAT0
S8	DAT1

Contact time sequence:



SW Circuit Description for Connector:

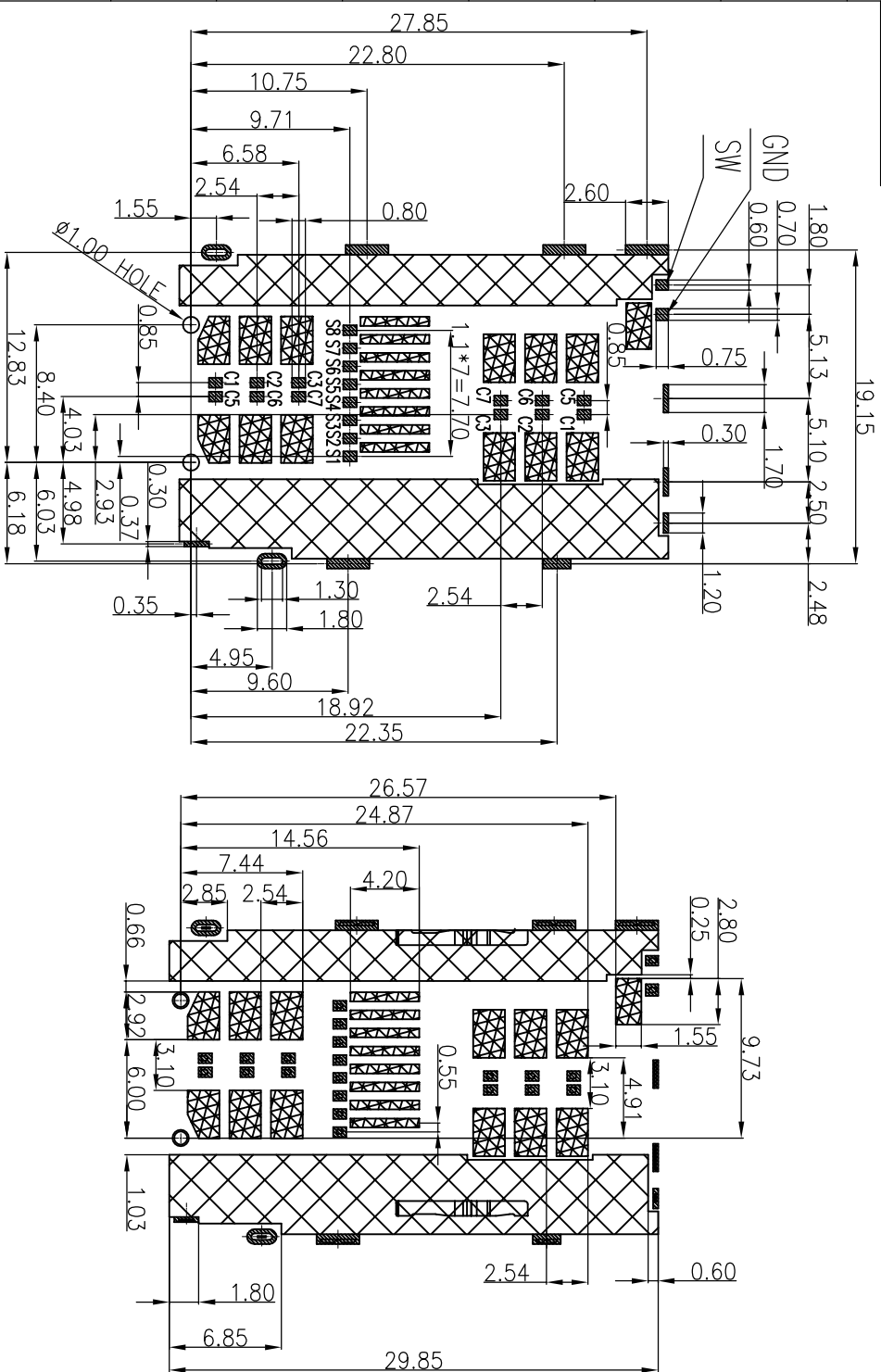
CARD INSERTION STATE	DETECT SWITCH CIRCUIT STATE	SCHEMATIC
TRAY UNMATED	OPENED	
TRAY MATED	CLOSED	

MEASURE POINT:	QUADRANT POINT	TANGENCY POINT	INTERSECTION POINT
UNLESS OTHERWISE SPECIFIED,TOLERANCE:	X	X x	X x x
	±0.25	±0.20	±0.15
			±3°

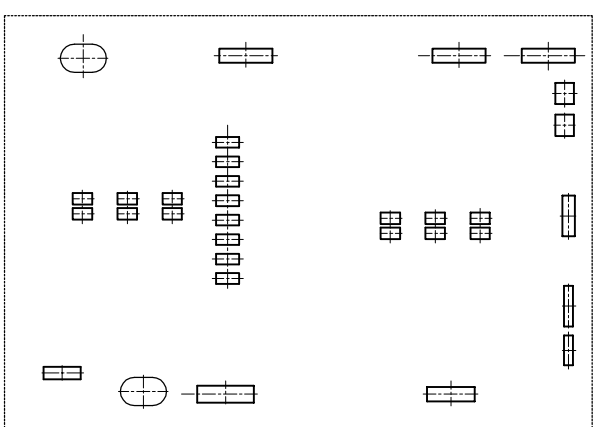
DESIGN	DATE	TITLE:
CHECKED	DATE	MICRO SIM+MICRO SD/Nano SIM
APPROVED	DATE	CONNECTOR(20pin) 145mm HEIGHT
PART NO.:	JYS-SIM145-128	REV: J
DRAWING NO.:	JYS-SIM145-128	SCALE: 1:1
		SIZE: A4
		SHEET: 2/4
		UNIT: mm

JYS 杰宇森电子有限公司
JIE YU SEN ELECTRONIC CO., LTD.




ROHS








RECOMMENDED PCB LAYOUT TOP VIEW (TOLERANCE:±0.05)



△ RECOMMENDED METAL MASK HOLE
 RECOMMENDED METAL MASK T=0.12MM

-  THESE AREA SHOULD BE FREE OF ANY CONDUCTIVE TRACES.
-  SMT SOLDER AREA.
-  KEEP OUT AREA (No Circuit)

MEASURE POINT:		
QUADRANT POINT		
TANGENCY POINT		
INTERSECTION POINT		
UNLESS OTHERWISE SPECIFIED, TOLERANCE:		
X	±0.25	
X.X	±0.20	
X.XX	±0.15	
ANGULAR	±3°	

 杰宇森电子有限公司 JIE YU SEN ELECTRONIC CO., LTD.		DESIGN	DATE
		CHECKED	DATE
PART NO.: JYS-SIM145-128		REV: J	SCALE: 1:1
DRAWING NO.: JYS-SIM145-128		SIZE: A4	SHEET: 3/4
		UNIT: mm	

